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		DISPLAY DEVICE DIVISION 5 DISPLAY DEVICE BUSINESS GROUP SHARP CORPORATION	

DEVICE SPECIFICATION FOR

TFT-LCD Cell

MODEL No. LK315T3HB94

CUSTOMER'S APPROVAL

DATE _____

BY _____

PRESENTED

By K. Chohka for

K. CHOHKA

DISPLAY DEVICE DIVISION 5

DISPLAY DEVICE BUSINESS GROUP

SHARP CORPORATION

SPEC No. : LD-K24Z13C

[illegible]



1. Application

This specification applies to the color 31.5" Wide XGA TFT Open-Cell LK315T3HB94.

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* Do not use the device for equipment that requires an extreme level of reliability, such as aerospace applications, telecommunication equipment (trunk lines), nuclear power control equipment and medical or other equipment for life support.

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* Contact and consult with a SHARP sales representative for any questions about this device.

2. Overview

This Open-Cell is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit, inverter circuit and back light system etc. Graphics and texts can be displayed on a 1366×RGB×768 dots panel with 16,777,216 colors by using LVDS (Low Voltage Differential Signaling) to interface, +12V of DC supply voltages.

And in order to improve the response time of LCD, this module applies the Over Shoot driving (O/S driving) technology for the control circuit. In the O/S driving technology, signals are being applied to the Liquid Crystal according to a pre-fixed process as an image signal of the present frame when a difference is found between image signal of the previous frame and that of the current frame after comparing them.

By using the captioned process, the image signals of this Open-Cell are being set so that image response can be completed within one frame, as a result, image blur can be improved and clear image performance can be realized.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	80.039 (Diagonal)	cm
	31.5 (Diagonal)	inch
Active area	697.69 (H) × 392.26 (V)	mm
Pixel Format	1366 (H) × 768 (V) (1pixel = R + G + B dot)	pixel
Pixel pitch	0.51075(H) × 0.51075 (V)	mm
Pixel configuration	R,G, B vertical stripe	
Display mode	Normally black	
Unit Outline Dimensions [Note 2]	715.59(W) × 446.45(H) × 1.8(D)	mm
Mass	1.15 ± 0.1	kg
Surface treatment [Note 1]	Low-Haze Anti Glare, Hard coating Surface Hardness; 2H: CF side (Front) <6B: TFT side (Rear)	

[Note 1] Without the protection film.

[Note 2] The Outline dimensions are shown in P17. ▲A



4. Input Terminals

4-1. TFT panel driving

CN1 (Interface signals and +12V DC power supply) (Shown in Fig.1)

Using connector : FI-X30SSL-HF-R2500 (JAE) ▲C

Matching connector : FI-X30H/FI-X30HL, FI-X30C/FI-X30C2L

or FI-X30M (Japan Aviation Electronics Ind. , Ltd.)

Matching LVDS transmitter : THC63LVDM83R (THine) or equivalent device

Pin No.	Symbol	Function	Remark
1	VCC	+12V Power Supply	
2	VCC	+12V Power Supply	
3	VCC	+12V Power Supply	
4	VCC	+12V Power Supply	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
8	GND	Ground	
9	SELLVDS	Select LVDS data order [Note 1]	Default: Pull down (L:GND) [Note 2]
10	Reserved	Not Available	
11	GND	Ground	
12	RIN0-	Negative (-) LVDS differential data input	LVDS
13	RIN0+	Positive (+) LVDS differential data input	LVDS
14	GND	Ground	
15	RIN1-	Negative (-) LVDS differential data input	LVDS
16	RIN1+	Positive (+) LVDS differential data input	LVDS
17	GND	Ground	
18	RIN2-	Negative (-) LVDS differential data input	LVDS
19	RIN2+	Positive (+) LVDS differential data input	LVDS
20	GND	Ground	
21	CLKIN-	Clock Signal(-)	LVDS
22	CLKIN+	Clock Signal(+)	LVDS
23	GND	Ground	
24	RIN3-	Negative (-) LVDS differential data input	LVDS
25	RIN3+	Positive (+) LVDS differential data input	LVDS
26	GND	Ground	
27	Reserved	No Connection	
28	Reserved	No Connection	
29	GND	Ground	
30	Reserved	No Connection	

[Note] GND of a liquid crystal panel drive part has connected with a module chassis.



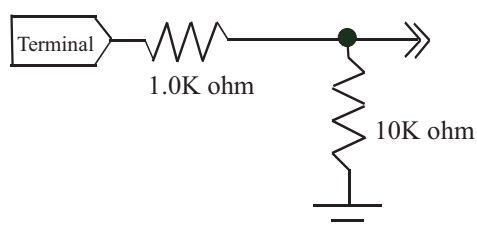
[Note1] SELLVDS

Transmitter		SELLVDS	
Pin No	Data	= L(GND) or Open	= H(3.3V)
51	TA0	R0(LSB)	R2
52	TA1	R1	R3
54	TA2	R2	R4
55	TA3	R3	R5
56	TA4	R4	R6
3	TA5	R5	R7(MSB)
4	TA6	G0(LSB)	G2
6	TB0	G1	G3
7	TB1	G2	G4
11	TB2	G3	G5
12	TB3	G4	G6
14	TB4	G5	G7(MSB)
15	TB5	B0(LSB)	B2
19	TB6	B1	B3
20	TC0	B2	B4
22	TC1	B3	B5
23	TC2	B4	B6
24	TC3	B5	B7(MSB)
27	TC4	NA	NA
28	TC5	NA	NA
30	TC6	DE(*)	DE(*)
50	TD0	R6	R0(LSB)
2	TD1	R7(MSB)	R1
8	TD2	G6	G0(LSB)
10	TD3	G7(MSB)	G1
16	TD4	B6	B0(LSB)
18	TD5	B7(MSB)	B1
25	TD6	NA	NA

NA: Not Available

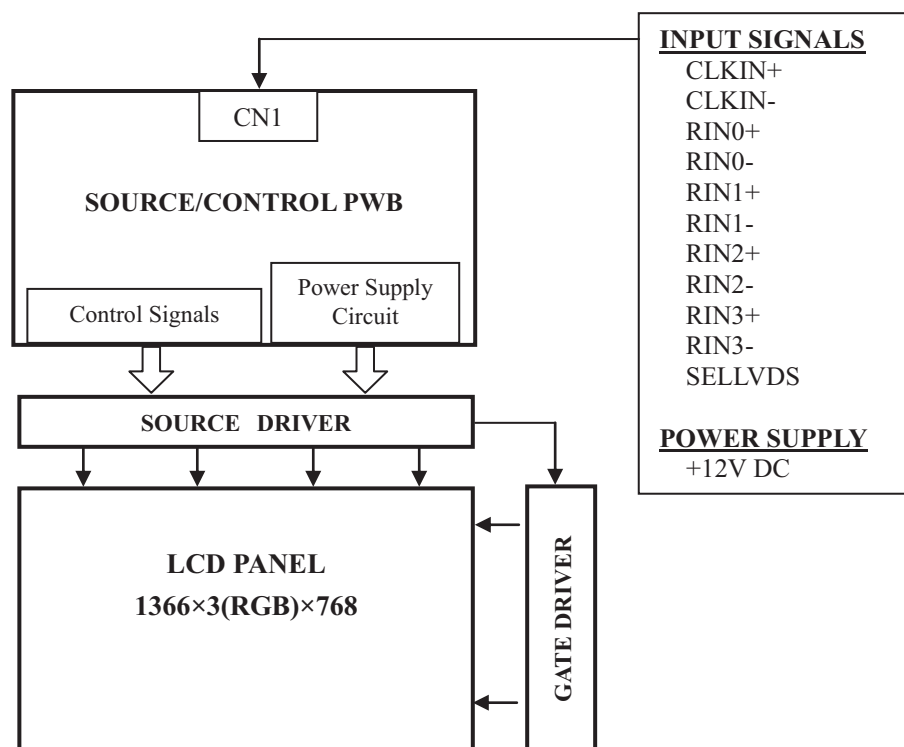
(*) Since the display position is prescribed by the rise of DE (Display Enable) signal, please do not fix DE signal during operation at "High."

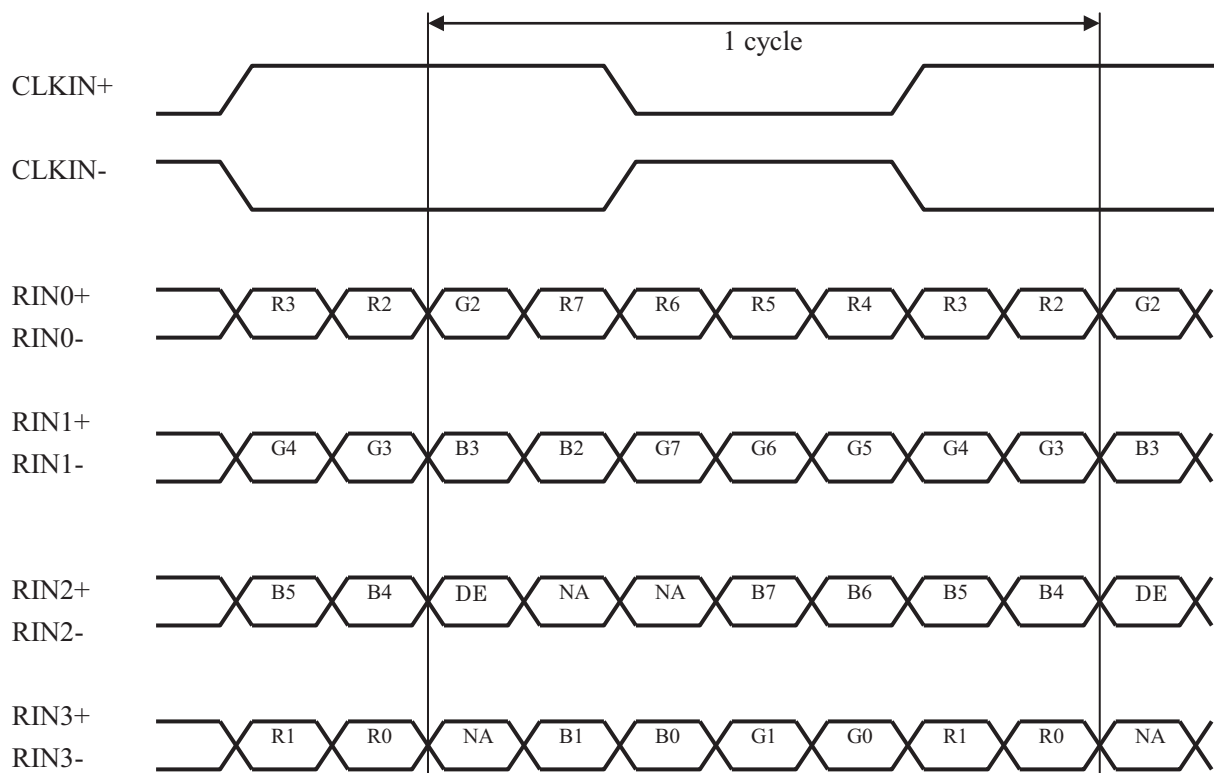
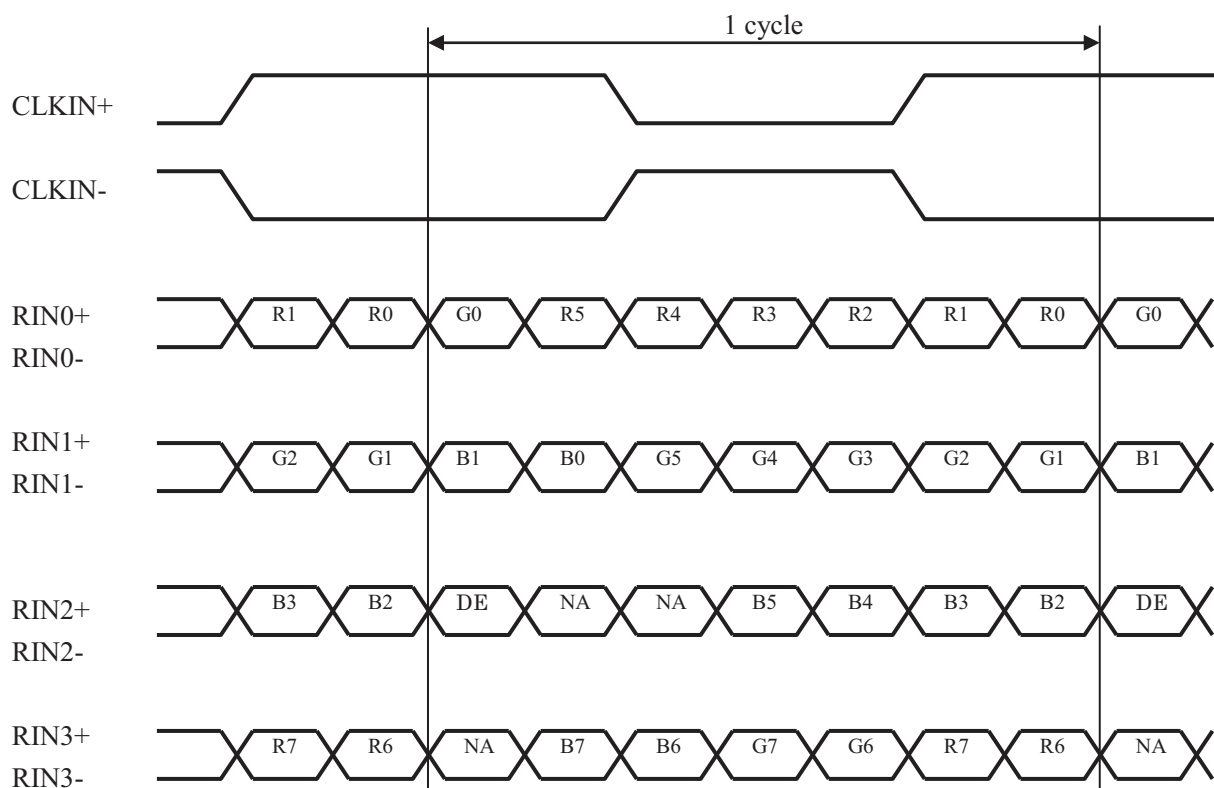
[Note 2] The equivalent circuit figure of the terminal





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• Block Diagram (LCD Module)

SELLVDS= High (3.3V)**SELLVDS= Low(GND) or Open**

DE: Display Enable

NA: Not Available (Fixed Low)

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage (for Control)	V_I	$T_a=25^{\circ}\text{C}$	-0.3 ~ 3.6	V	[Note 1]
+12V supply voltage (for Control)	V_{CC}	$T_a=25^{\circ}\text{C}$	0 ~ +15	V	
Storage temperature	T_{stg}	-	-25 ~ +60	$^{\circ}\text{C}$	[Note 2]
Operation temperature (Ambient)	T_{opa}	-	0 ~ +50	$^{\circ}\text{C}$	

[Note 1] SELLVDS

[Note 2] Humidity 95%RH Max.($T_a \leq 40^{\circ}\text{C}$)

Maximum wet-bulb temperature at 39°C or less.($T_a > 40^{\circ}\text{C}$), No condensation.

6. Electrical Characteristics

6-1. Control circuit driving

$T_a=25^{\circ}\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
+12V supply voltage	Supply voltage	V_{CC}	+11.4	+12.0	+13.7	V
	Current dissipation	I_{CC}	-	350	600	mA
		I_{RUSH}	-	1500	2500	mA
		T_{RUSH}	-	0.5	-	ms
Permissible input ripple voltage	V_{RP}	-	-	100	mV _{P-P}	$V_{CC} = +12.0\text{V}$
Differential input threshold voltage	High	V_{TH}	-	-	100	mV
	Low	V_{TL}	-100	-	-	mV
Input Low voltage	V_{IL}	0	-	0.7	V	[Note 3]
Input High voltage	V_{IH}	2.6	-	3.3	V	
Input leak current (Low)	I_{IL}	-	-	400	μA	$V_I = 0\text{V}$ [Note 3]
Input leak current (High)	I_{IH}	-	-	100	μA	$V_I = 3.3\text{V}$ [Note 3]
Terminal resistor	R_T	-	100	-	Ω	Differential input

[Note] V_{CM} : Common mode voltage of LVDS driver.

[Note 1]

Input voltage sequences

$$50\mu\text{s} < t_1 \leq 20\text{ms}$$

$$20\text{ms} < t_{2-1} \leq 5\text{s}$$

$$20\text{ms} < t_{2-2} \leq 5\text{s}$$

$$0 < t_3 \leq 1\text{s}$$

$$t_4 \geq 1\text{s}$$

$$t_5 \geq 300\text{ms}$$

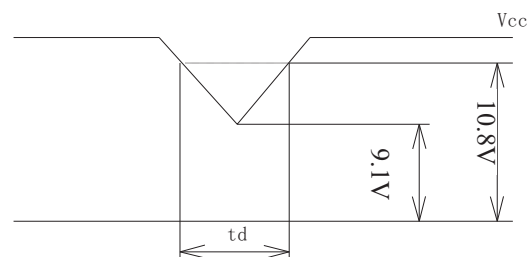
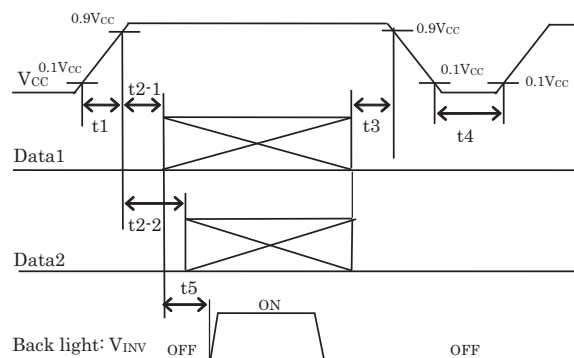
Dip conditions for supply voltage

$$\text{a) } 9.1\text{V} \leq V_{CC} < 10.8\text{V}$$

$$t_d \leq 10\text{ms}$$

$$\text{b) } V_{CC} < 9.1\text{V}$$

Dip conditions for supply voltage is based on input voltage sequence.

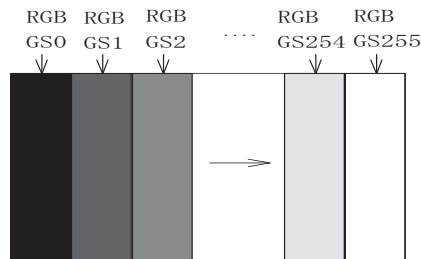


- ※ Data1: CLKIN $\pm$, RIN0 $\pm$, RIN1 $\pm$, RIN2 $\pm$, RIN3 $\pm$
- ※ Data2: SELLVDS
- ※ About the relation between data input and back light lighting, please base on the above-mentioned input sequence.

When back light is switched on before panel operation or after a panel operation stop, it may not display normally. But this phenomenon is not based on change of an incoming signal, and does not give damage to a liquid crystal display.

[Note 2] Typical current situation: 256 gray-bar pattern ($V_{CC} = +12.0V$)

The explanation of RGB gray scale is seen in section 8.

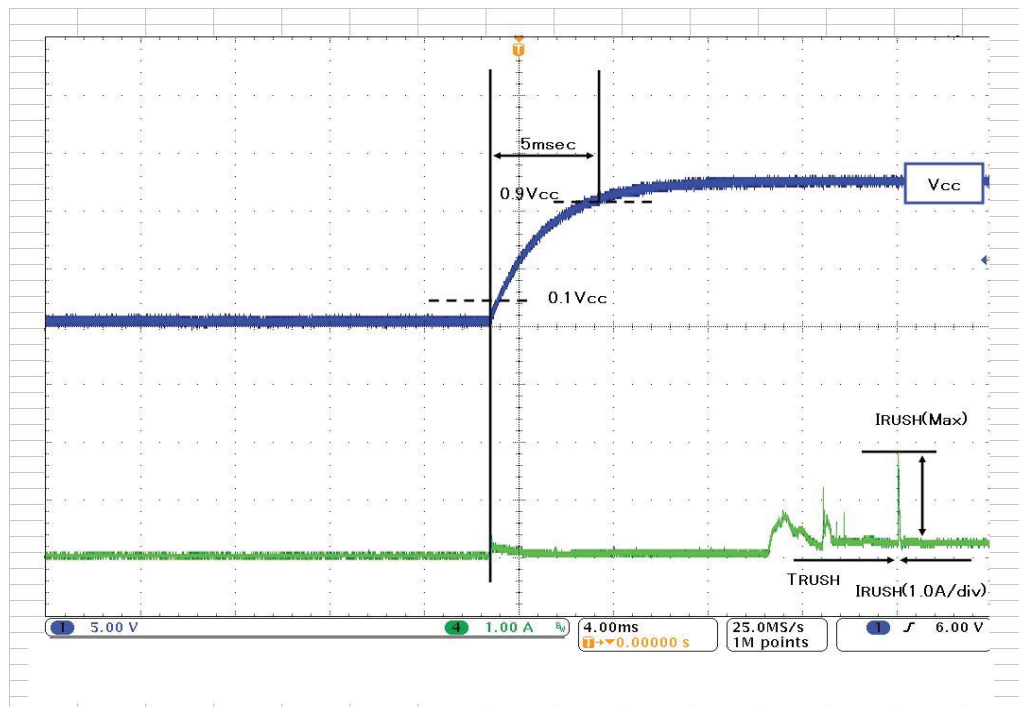


$V_{CC} = +12.0V$
 $CK = 82.0MHz$
 $Th = 20.68\mu s$

[Note 3] SELLVDS

[Note 4] CLKIN+/CLKIN-, RIN0+/RIN0-, RIN1+/RIN1-, RIN2+/RIN2-, RIN3+/RIN3-

[Note 5] The Rush current corrugation at the time of power on



7. Timing characteristics of input signals

7-1. Timing characteristics

Timing diagrams of input signal are shown in Fig.2

Parameter		Symbol	Min.	Typ.		Max.	Unit
				NTSC	PAL		
Clock	Frequency	1/Tc	72	82	82	85	MHz
Data enable signal	Horizontal period	TH	1540	1696	1696	1940	clock
			19.84	20.68	20.68	-	μs
	Horizontal period (High)	THd	1366	1366	1366	1366	clock
	Vertical period	TV	778	806	967	972	line
	Vertical period (High)	TVd	768	768	768	768	line

[Note] When vertical period is very long, flicker may occur.

Please turn off the module after it shows the black screen.

Please make sure that length of vertical period should become of an integral multiple of horizontal length of period. Otherwise, the screen may not display properly.

As for your final setting of driving timing, we will conduct operation check test at our side, please inform your final setting.

Vertical period must change less than 1 line each frames.

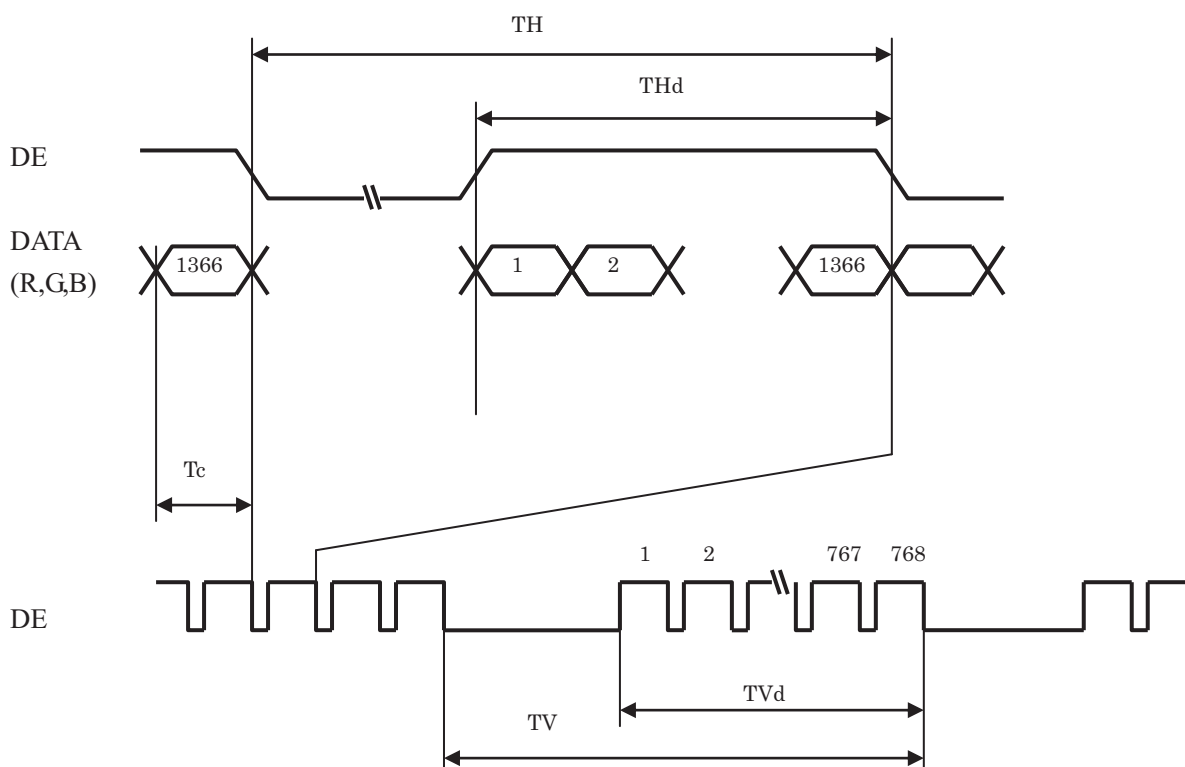
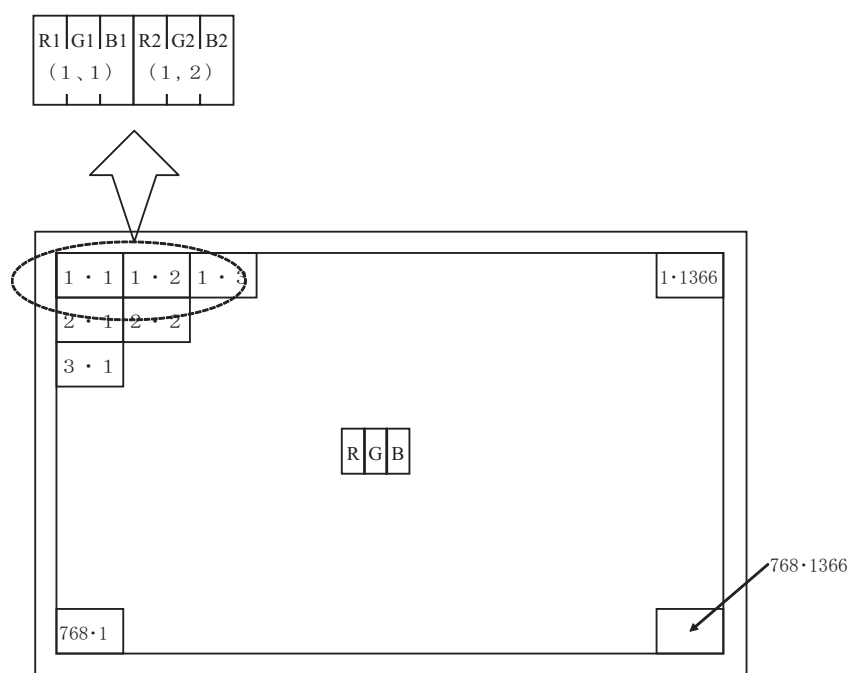


Fig.2 Timing characteristics of input signals



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7-2. Input data signal and display position on the screen



Display Position of Data (V,H)



8. Input Signal, Basic Display Colors and Gray Scale of Each Color

	Colors & Gray scale	Gray Scale	Data signal																							
			R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7
Basic Color	Black	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	—	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	—	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	—	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	—	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	—	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	—	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	↓	↓								↓								↓							
	↓	↓	↓								↓								↓							
	Brighter	GS253	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↓	GS254	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	↓	↓								↓								↓							
	↓	↓	↓								↓								↓							
	Brighter	GS253	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	↓	GS254	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	↑	↓	↓								↓								↓							
	↓	↓	↓								↓								↓							
	Brighter	GS253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1
	↓	GS254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	Blue	GS255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

0 : Low level voltage, 1 : High level voltage.

Each basic color can be displayed in 256 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16,777,216 colors display can be achieved on the screen.

9. Optical characteristics

 $T_a = 25^{\circ}\text{C}$, $V_{cc} = +12\text{V}$

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing angle range	Horizontal	θ_{21} θ_{22}	$CR \geq 10$	70	88	-	Deg.	[Note1,4]
	Vertical	θ_{11} θ_{12}		70	88	-	Deg.	
Contrast ratio		CRn	$\theta = 0 \text{ deg.}$	3750	5000	-	-	[Note2,4]
Response time		τ_{DRV}		-	7	-	ms	[Note3,4,5]
Chromaticity of white ▲▲		x		0.246	0.276	0.306	-	[Note 4]
		y		0.244	0.274	0.304	-	
Chromaticity of red ▲▲		x		0.603	0.633	0.663	-	
		y		0.324	0.354	0.384	-	
Chromaticity of green ▲▲		x		0.288	0.318	0.348	-	
		y		0.625	0.655	0.685	-	
Chromaticity of blue ▲▲		x		0.115	0.145	0.175	-	
		y		0.021	0.051	0.081	-	
Luminance of white		Y_L		320	400	-	cd/m^2	[Note 4]
Luminance uniformity		δ_w		-	-	1.25	-	[Note 6]

Measurement condition : Back Light Unit is based on SHARP standard Moudel.

*The measurement shall be executed 60 minutes after lighting at rating.

[Note] The optical characteristics are measured using the following equipment.

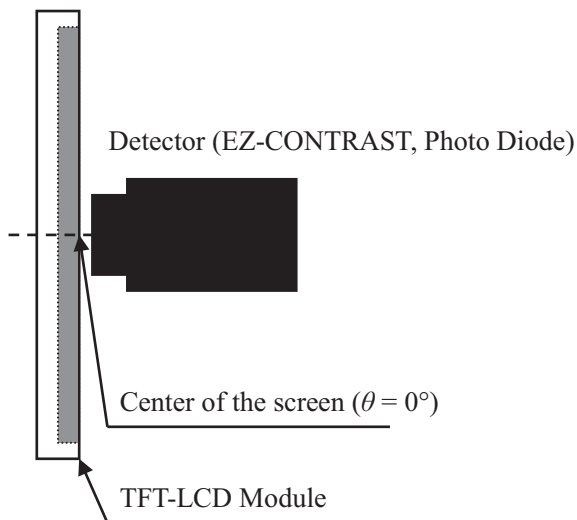


Fig.3-1 Measurement of viewing angle range and response time.
(Viewing angle range: EZ-CONTRAST
Response time: Photo Diode)

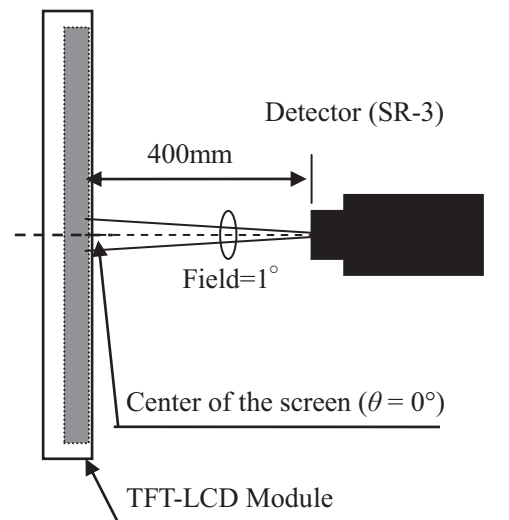
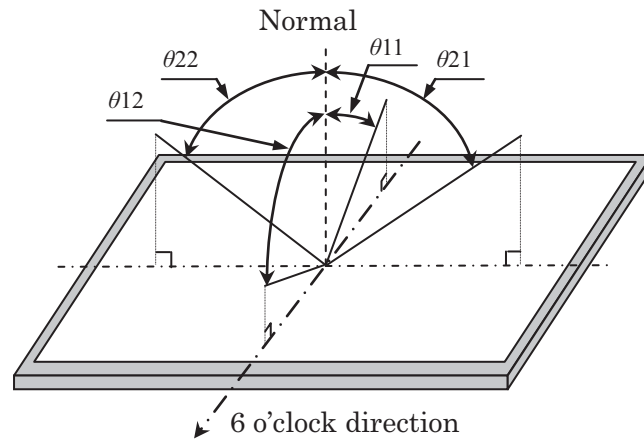


Fig.3-2 Measurement of Contrast, Luminance, and Chromaticity.

[Note 1] Definitions of viewing angle range :



[Note 2] Definition of contrast ratio :

The contrast ratio is defined as the following.

$$\text{Contrast Ratio} = \frac{\text{Luminance (brightness) with all pixels white}}{\text{Luminance (brightness) with all pixels black}}$$

[Note 3] Definition of response time

The response time (τ_{DRV}) is defined as the following figure and shall be measured by switching the input signal for “any level of gray (0%, 25%, 50%, 75% and 100%)” and “any level of gray (0%, 25%, 50%, 75% and 100%)” at panel surface temperature 45°C.

	0%	25%	50%	75%	100%
0%		tr: 0%-25%	tr: 0%-50%	tr: 0%-75%	tr: 0%-100%
25%	td: 25%-0%		tr: 25%-50%	tr: 25%-75%	tr: 25%-100%
50%	td: 50%-0%	td: 50%-25%		tr: 50%-75%	tr: 50%-100%
75%	td: 75%-0%	td: 75%-25%	td: 75%-50%		tr: 75%-100%
100%	td: 100%-0%	td: 100%-25%	td: 100%-50%	td: 100%-75%	

t*:x-y...response time from level of gray(x) to level of gray(y)

$$\tau_{\text{DRV}} = \Sigma(t^*:x-y)/20$$

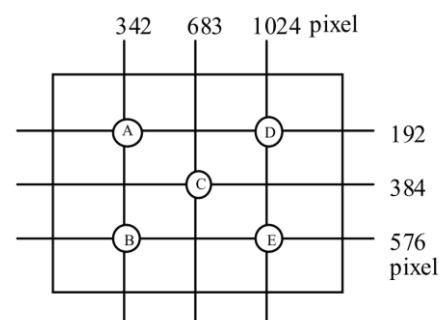
[Note 4] This shall be measured at center of the screen.

[Note 5] This value is valid when O/S driving is used at typical input time value .

[Note 6] Definition of white uniformity ;

White uniformity is defined as the following with five measurements. (A~E)

$$\delta_w = \frac{\text{Maximum luminance of five points (brightness)}}{\text{Minimum luminance of five points (brightness)}}$$



10. Reliability

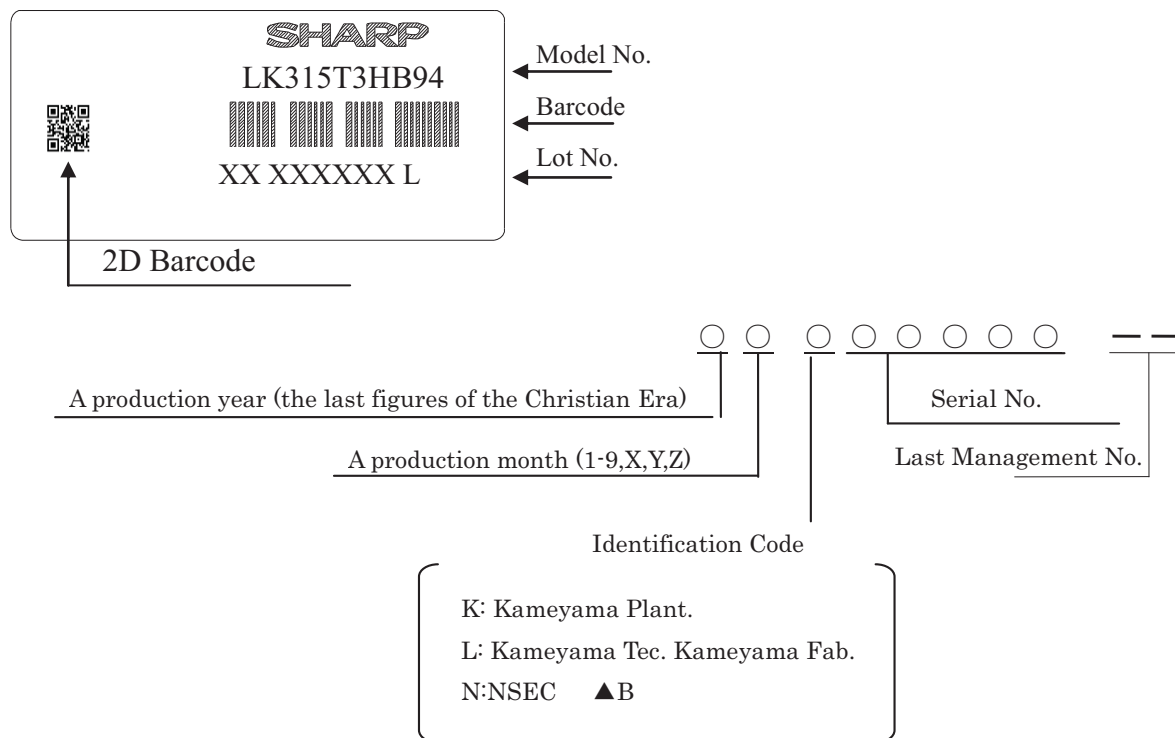
Reliability test item

No.	Test item	Condition
1	High temperature storage test	Ta=60°C 500h
2	Low temperature storage test	Ta=-25°C 500h
3	High temperature and high humidity operation test	Ta=40°C ; 95%RH 500h (No condensation)
4	High temperature operation test	Ta=50°C 500h (The panel surface temperature of this time is MAX60°C)
5	Low temperature operation test	Ta=0°C 500h (The panel surface temperature of this time is MIN0°C)
6	Vibration test (Cell Box with Open-Cells) (non-operation)	X and Y direction: 15min, Z direction: 60min. 5Hz to 50Hz acceleration velocity: 1.0G Sweeping ratio: 3min
7	Shock test (Cell Box with Open-Cells) (non-operation)	Maximum acceleration: 490m/s ² Pulse width: 11ms, sinusoidal half wave Direction: +/-X, +/-Y, +/-Z, once for each direction.

11. Label

1. Lot No. label

The label stuck on a cell surface displays SHARP, product model (LK315T3HB94) and a product Lot No.
(ex.) [LK315T3HB94] JAPAN PRODUCTION



LD-K24Z13C-14

2. Packing label

• Cell box

社内品番:	LK315T3HB94*	(①)
Bar code		
Lot NO. • (1 T) * * * *. * . * * (②)		
Bar code		
Quantity:	(Q)	15 pcs (③)
Bar code		
ユーザ品番		
Bar code		
シャープ物流用ラベルです。		

• Carton

社内品番:	LK315T3HB94*	(①)
Bar code		
Lot NO. • (1 T) * * * *. * . * * (②)		
Bar code		
Quantity:	(Q)	240 pcs (③)
Bar code		
ユーザ品番		
Bar code		
シャープ物流用ラベルです。		

① Management No

② Lot No. (Date)

③ Quantity

12. Packing form ▲C

- Piling number of cartons: 16 cell box / 1 palette.
- Packing quantity in one cell box: 15 pcs
- Carton size①: 1200(W) × 1000(D) × 1127(H)
Carton size②: 1200(W) × 1000(D) × 1157(H)
- Total mass of one carton filled with full modules: 330 kg(Max)

13. Carton storage condition

- Temperature: 0°C to 40°C
- Humidity: 95%RH or less
Reference condition: 20°C to 35°C, 85%RH or less (summer)
: 5°C to 15°C, 85%RH or less (winter)
The total storage time (40°C, 95%RH): 240H or less
- Sunlight:
Be sure to shelter a product from the direct sunlight.
- Atmosphere:
Do not store in a place where exists the risk of corrosive gas (such as acid and alkali) or volatile solvents.
- Prevent condensation:
Be sure to put cartons on a palette or base, don't put it on the floor, and store them keeping off the wall.
Please take care of ventilation in storehouse and around cartons, and control temperature not to change abruptly beyond the natural environment.
- Storage life: 6 months

14. Precautions

- a) Because the Open-Cell is weak to static electricity, please do not touch the terminal with bare hands.
- b) Since the front polarizer is easily damaged, pay attention not to scratch it.
- c) Since long contact with drops of water may cause discoloration or spots, please wipe off them as soon as put on the screen.
- d) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- e) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- f) Precautions of peeling off the protection film:
 - Be sure to peel off slowly (recommended more than 7sec) and constant speed.
 - Peeling direction shown in Fig. 5.
 - Be sure to ground person with adequate methods such as the anti-static wrist band.
 - Be sure to ground S-PWBs while peeling off the protection film.
 - Ionized air should be blown to the surface while peeling off.
 - The protection film must not touch drivers and S-PWBs.
 - If adhesive may remain on the polarizer after the protection film peeled off, please remove with isopropyl-alcohol.

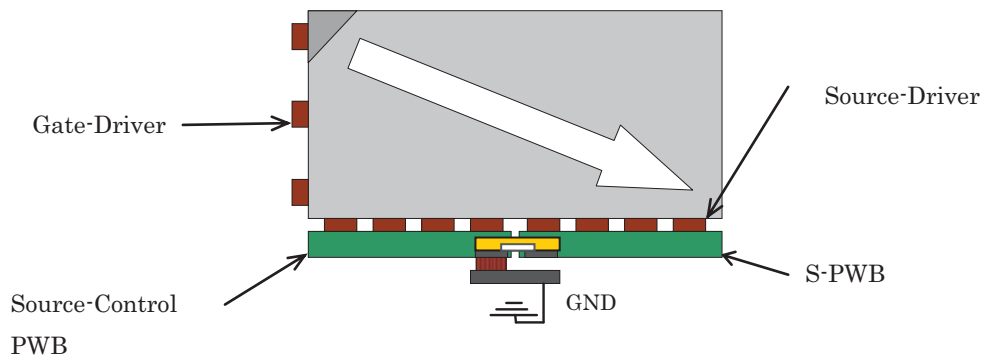


Fig.5 Direction of peeling off

- g) Since the Open-Cell consists of TFT and electronic circuits with CMOS-ICs, which are very weak to electrostatic discharge, persons who are handling a Open-Cell should be grounded through adequate methods such as an anti-static wrist band. Connector pins should not be touched directly with bare hands.

•Reference: Process control standard of sharp

	item	Management standard value and performance standard
1	Anti-static mat(shelf)	1 to 50 [Mega ohm]
2	Anti-static mat(floor, desk)	1 to 100 [Mega ohm]
3	Ionizer	Attenuate from $\pm 1000V$ to $\pm 100V$ within two seconds.
4	Anti-static wrist band	0.8 to 10 [Mega ohm]
5	Anti-static wrist band entry and ground resistance	Below 1000 [ohm]
6	Temperature	22 to 26 [°C]
7	Humidity	60 to 70 [%]

- h) Since the Open-Cell has some PWBs, please take care to keep them off any stress or pressure when handling or installing the Open-Cell, otherwise some of electronic parts on them may be damaged.
- i) Be sure to turn off the power supply when inserting or disconnecting the cable.
- j) Be sure to design the module and cabinet so that the Open-Cell can be installed without any extra stress such as warp or twist.



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- k) When handling and assembling Open-Cells into module and cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of materials such as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the Open-Cell.
- l) Applying too much force and stress to PWBs and drivers may cause a malfunction electrically and mechanically.
- m) The Open-Cell has high frequency circuits. Sufficient suppression to EMI should be done by system manufactures.
- n) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- o) The chemical compound, which causes the destruction of ozone layer, is not being used.
- p) This Open-Cell module is corresponded to RoHS.
- q) Please design the heat dissipation of the module with enough care for SC-PWB, Source-driver and Gate-driver's IC.
- r) When any question or issue occurs, it shall be solved by mutual discussion.

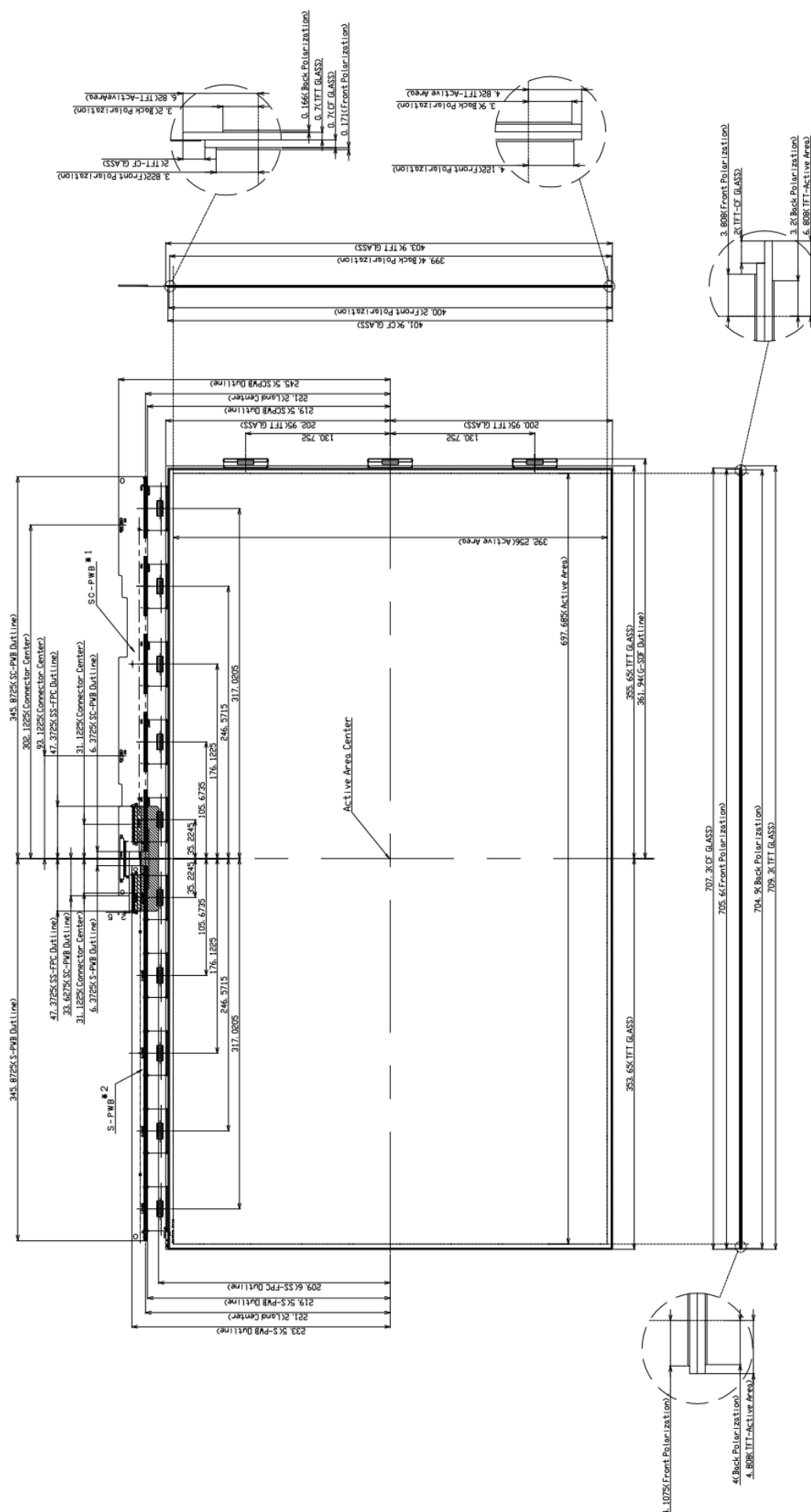


Fig. 6 Outline dimensions $\blacktriangle$ A

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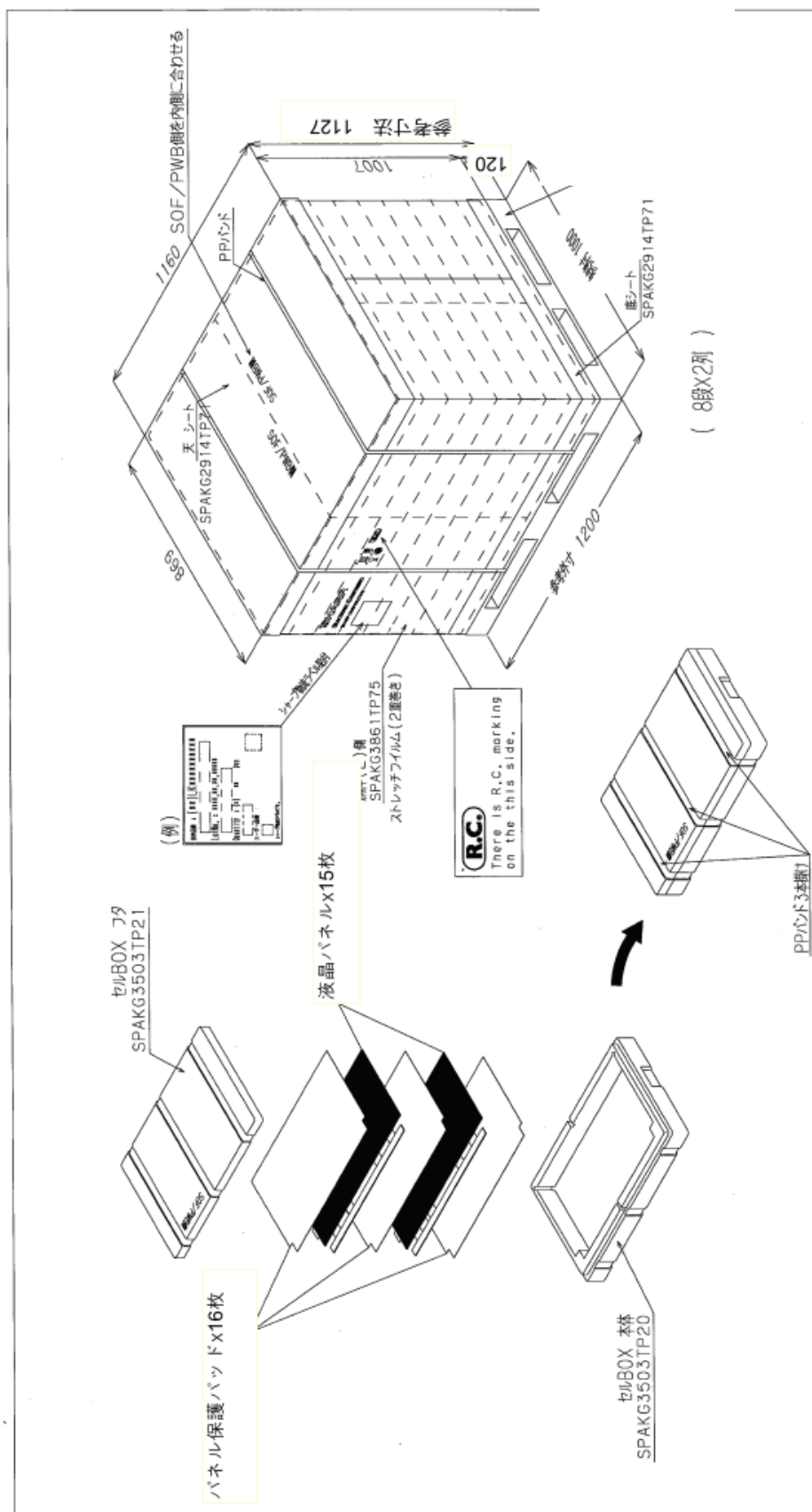


fig.7 Packing form① for LK315T3HB94 ▲C

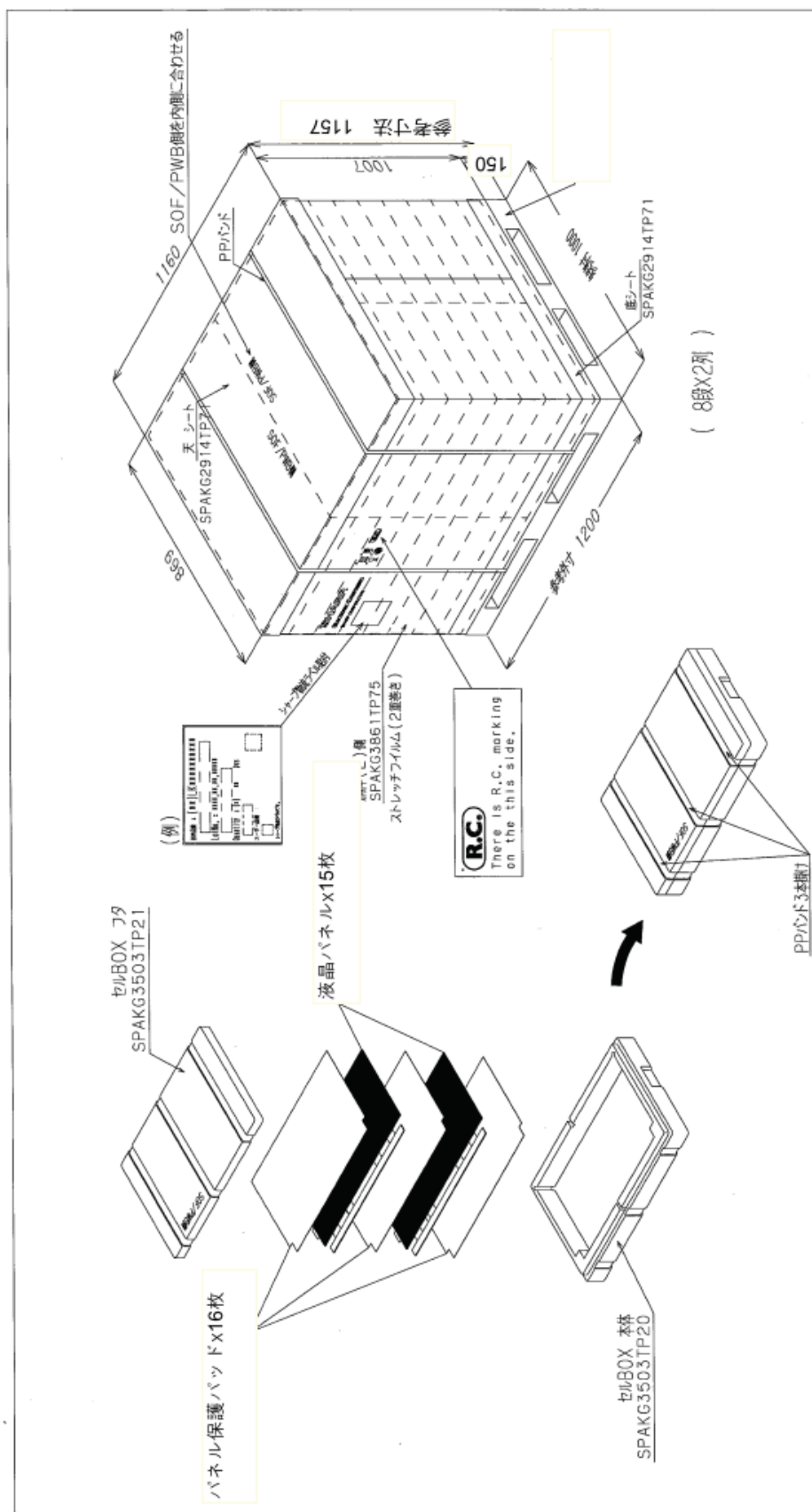


fig.8 Packing form② for LK315T3HB94 ▲C